



**PTC04-DB-81106**

**Short Description**

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## 1. Scope

This document is intended to give a brief introduction of the PTC04-DB-81106 module.

Beside of this document, several other important documentation papers are necessary for a detailed understanding.

The detailed information and the required software tools regarding our products including all required development tools will be distributed via the Melexis Softdist server (<https://softdist.melexis.com>).

## 2. Melexis Softdist Server

Melexis SoftDist (<https://softdist.melexis.com>) is a software distribution system which allows customers to download documents, development software and other stuff related to Melexis products. In case updates or new items are available a notification email will be send automatically to all subscribers.

It's required to register in order to access the Melexis Softdist server.

In case you are not registered yet, please contact our sales team and specify which Melexis product you are interested in, in order to create an account and grant access to the correct product specific information:

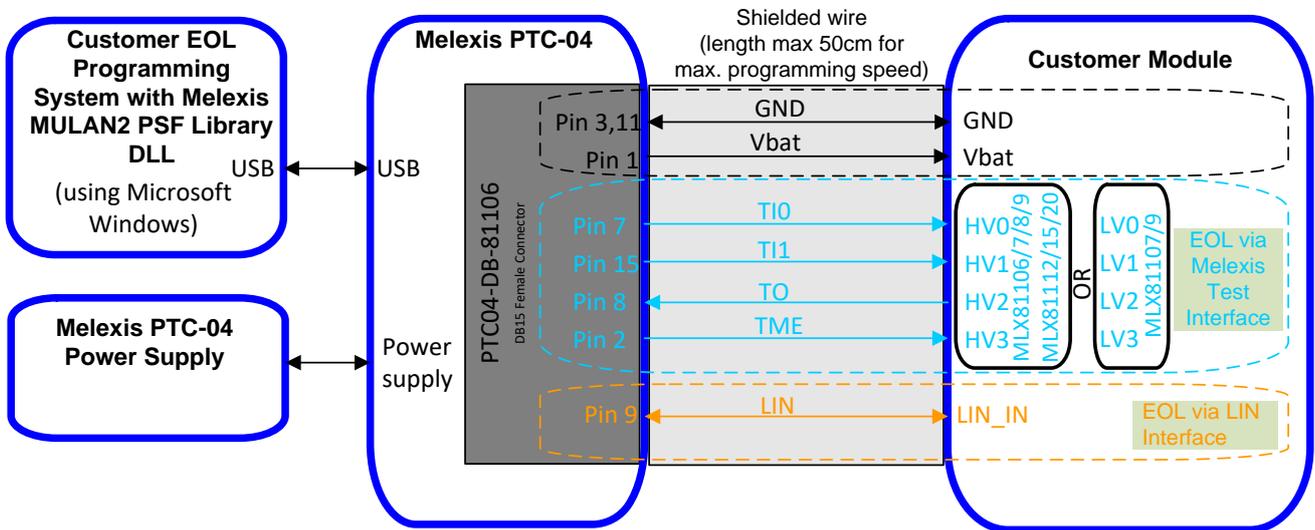
Europe, Africa	Email : sales_europe@melexis.com
Americas	Email : sales_usa@melexis.com
Asia	Email : sales_asia@melexis.com

### 3. General Description

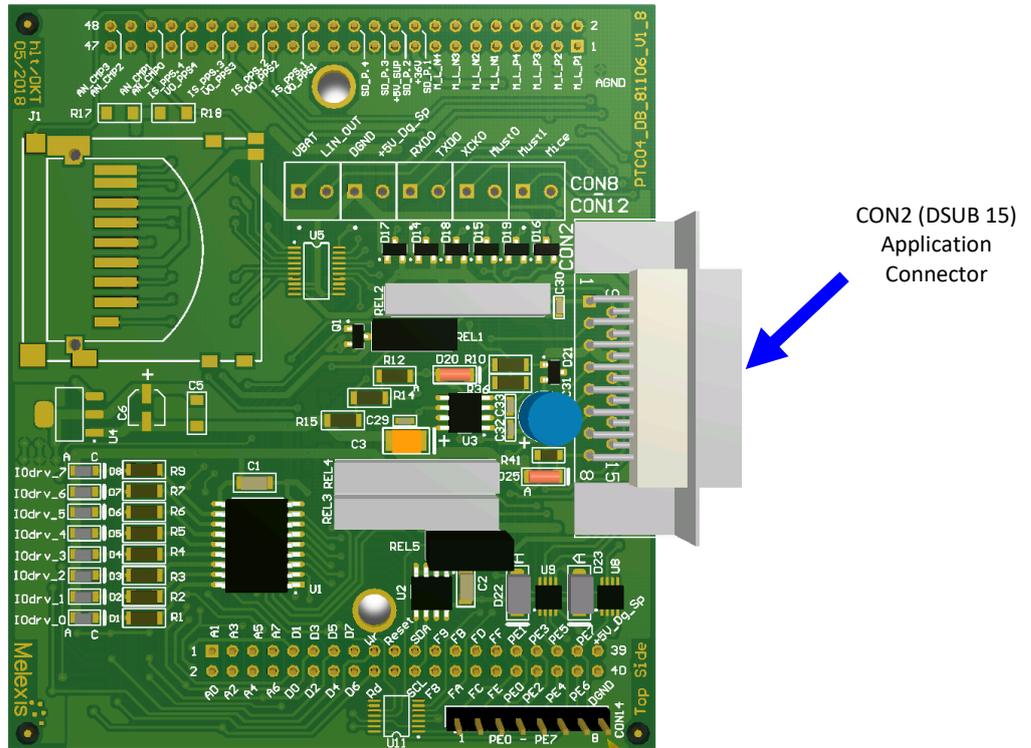
The PTC04-DB-81106 is a daughter board for the Melexis PTC-04 EOL programmer. Together with the PTC-04 it supports the end of line programming of following Melexis components:

- MLX81106KDC
- MLX81107KLW
- MLX81108KDC
- MLX81109KLW
- MLX80110KDC
- MLX81112KDC
- MLX81115KLW
- MLX81120KLW

The PTC04-DB-81106 needs to be mounted into PTC-04. This module supports the programming via standard LIN, Fast protocol and the MLX test interface.



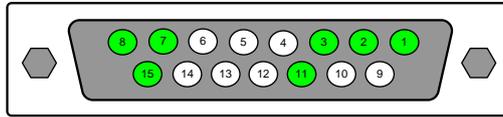
## 4. Hardware overview



## 4.1. PTC04-DB-81106 CON2 Application Connector

### 4.1.1. Programming via the Melexis Test Interface

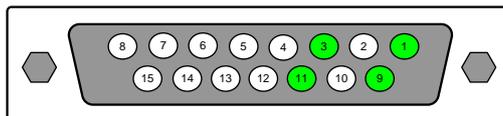
All unused pins of the connector have to be left open. Only the pins with the green color and the shield have to be used for the Melexis test interface.



Pins	Name	Description	MLX81106/7/8/9 MLX81112/15/20 Using HV Pins	MLX81107/9 Using LV Pins
1	<b>VBAT</b>	<b>Power Supply Vbat</b>	<b>Vbat</b>	<b>Vbat</b>
2	<b>VOUT_PPS2</b>	<b>Test Mode Enable TME</b>	<b>HV3</b>	<b>LV3</b>
3	<b>DGND</b>	<b>Digital Ground GND</b>	<b>GND</b>	<b>GND</b>
4	MUST	Analog test pin		
5	TXD0	USART Transmit Data		
6	VOUT_PPS3	VOUT_PPS3		
7	<b>MUST0 / TIO</b>	<b>Digital test pin TIO</b>	<b>HV0</b>	<b>LV0</b>
8	<b>MICE / TO</b>	<b>Digital test pin TO</b>	<b>HV2</b>	<b>LV2</b>
9	<b>LIN_OUT</b>	<b>LIN BUS</b>		
10	+5V_DIG_CON	+5V DC Supply		
11	<b>DGND</b>	<b>Digital Ground GND</b>	<b>GND</b>	<b>GND</b>
12	RXD0	USART Receive Data		
13	XCK0	USART Clock		
14	N.C.	Not connected		
15	<b>MUST1 / TI1</b>	<b>Digital test pin TI1</b>	<b>HV1</b>	<b>LV1</b>

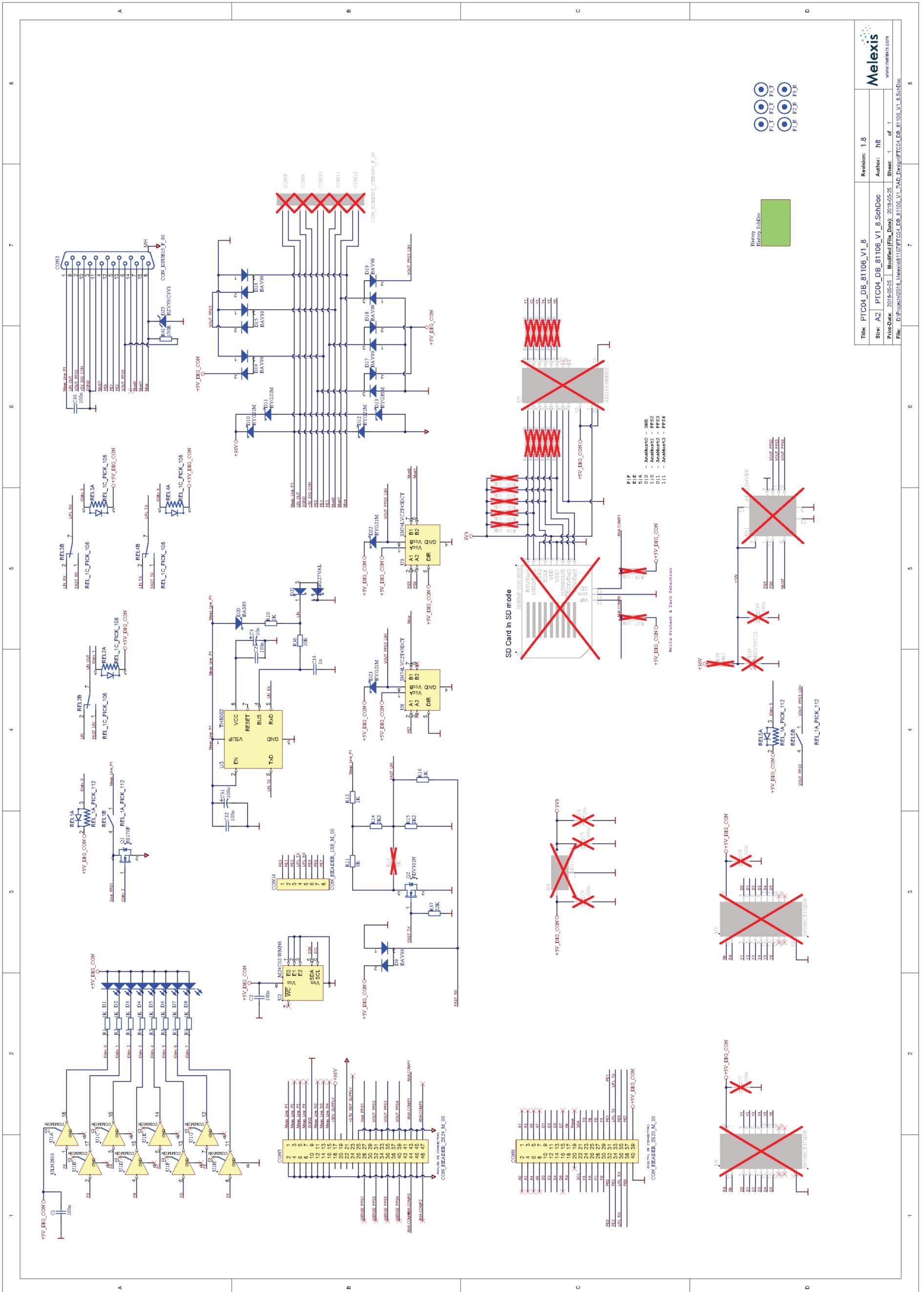
### 4.1.2. Programming via the LIN Interface

All unused pins of the connector have to be left open. Only the pins with the green color have to be used for the LIN bus interface.



Pins	Name	Description	MLX81106/07/08/09/12/15/20
1	<b>VBAT</b>	<b>Power Supply Vbat</b>	<b>Vbat</b>
2	VOUT_PPS2	Test Mode Enable TME	
3	<b>DGND</b>	<b>Digital Ground GND</b>	<b>GND</b>
4	MUST	Analog test pin	
5	TXD0	USART Transmit Data	
6	VOUT_PPS3	VOUT_PPS3	
7	MUST0 / TIO	Digital test pin TIO	
8	MICE / TO	Digital test pin TO	
9	<b>LIN_OUT</b>	<b>LIN BUS</b>	<b>LIN_IN</b>
10	+5V_DIG_CON	+5V DC Supply	
11	DGND	Digital Ground GND	
12	RXD0	USART Receive Data	
13	XCK0	USART Clock	
14	N.C.	Not connected	
15	MUST1 / TI1	Digital test pin TI1	

## 5. Schematic



## 6. Revision History

Version	Changes	Remark	Date
001		Initial release	25.07.18
002	Correct document header and footer		12.11.21

## 7. Disclaimer

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